T. M	umber	Hits	Search Text	DB	Time stamp
1	unber	38	(soft adj macro) and ((hard adj macro) or	USPAT;	2003/06/03 12:59
1		30	(firm adj macro))	US-PGPUB;	
			(IIIIII ad) illacio,	EPO; JPO;	
1				DERWENT;	
1					
				IBM_TDB	2002/06/02 12 00
2		2139	interconnect\$3 adj3 information	USPAT;	2003/06/03 13:00
				US-PGPUB;	
-				EPO; JPO;	
1				DERWENT;	
				IBM_TDB	
3		5274	optimiz\$3 adj4 design	USPAT;	2003/06/03 13:01
1				US-PGPUB;	
				EPO; JPO;	
		,		DERWENT;	
				IBM TDB	
4		274323	optimiz\$3	USPAT;	2003/06/03 13:01
*		2,1323	000111111111	US-PGPUB;	,,
			• •	EPO; JPO;	
			·	DERWENT;	
_		1.00-	landinan nain impampua nain dimedik	IBM_TDB	2002/06/02 12:02
5	-	11000	design\$3 adj3 integrated adj3 circuit	USPAT;	2003/06/03 13:02
				US-PGPUB;	. ,
				EPO; JPO;	
	-			DERWENT;	
ļ			<u> </u>	IBM_TDB	
6		10911	bandwidth and latency	USPAT;	2003/06/03 13:03
		,	·	US-PGPUB;	
				EPO; JPO;	
		•		DERWENT;	
		-		IBM TDB	
7		8611	(design adj3 environment) or (design adj3	USPAT;	2003/06/03 13:04
'	· ·		development)	US-PGPUB;	,,
			acveropmente,	EPO; JPO;	
				DERWENT;	
				IBM TDB	
		300	(ontimises adia decien) and (decience adia		2003/06/03 13:04
8		390	(optimiz\$3 adj4 design) and (design\$3 adj3	USPAT;	2003/00/03 13:04
			integrated adj3 circuit)	US-PGPUB;	
				EPO; JPO;	·
		-		DERWENT;	
				IBM_TDB	0000/05/00 10 5:
9		107		USPAT;	2003/06/03 13:04
			development)) and ((optimiz\$3 adj4 design)	US-PGPUB;	
			and (design\$3 adj3 integrated adj3 circuit))	EPO; JPO;	
1				DERWENT;	ļ.•
				IBM_TDB	
10		0	(((design adj3 environment) or (design adj3	USPAT;	2003/06/03 13:05
			development)) and ((optimiz\$3 adj4 design)	US-PGPUB;	
			and (design\$3 adj3 integrated adj3	EPO; JPO;	
1			circuit))) and (bandwidth and latency)	DERWENT;	
1				IBM TDB	
11		127	(interconnect\$3 adj3 information) and	USPAT;	2003/06/03 13:05
1		12/	(bandwidth and latency)	US-PGPUB;	, ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
			\Landamadeir dild, ideeliey/	EPO; JPO;	
				DERWENT;	
			1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	IBM_TDB	2002/06/02 12:07
12		3	(optimiz\$3 adj4 design) and ((interconnect\$3	USPAT;	2003/06/03 13:07
			adj3 information) and (bandwidth and	US-PGPUB;	
1			latency))	EPO; JPO;	
				DERWENT;	
			·	IBM_TDB	
13		45	(interconnect\$3 adj3 information) and	USPAT;	2003/06/03 13:07
			(optimiz\$3 adj4 design)	US-PGPUB;	
				EPO; JPO;	
1			·	DERWENT;	
				IBM TDB	
14		24	(design\$3 adj3 integrated adj3 circuit) and	USPAT;	2003/06/03 13:08
1 -4		44	((interconnect\$3 adj3 information) and	US-PGPUB;	=====================================
				EPO; JPO;	
			(optimiz\$3 adj4 design))		
1				DERWENT;	
1		1		IBM_TDB	

	_					
Γ.	-		5235	(optimiz\$3 adj4 integrate adj2 circuit) or	USPAT;	2003/06/03 07:28
				(optimiz\$3 adj3 design\$3)	US-PGPUB;	
					EPO; JPO;	
					DERWENT;	
				•	IBM TDB	
			0	self-programmable adj3 integrated adj2	USPAT;	2003/06/02 14:54
'	-		١	circuit	US-PGPUB;	2
				CITCUIT	EPO; JPO;	*
					DERWENT;	
					IBM_TDB	0000/06/00 14 55
•	-		394	(design\$3 adj4 integrated adj circuit) and	USPAT;	2003/06/02 14:57
				((optimiz\$3 adj4 integrate adj2 circuit) or	US-PGPUB;	
				(optimiz\$3 adj3 design\$3))	EPO; JPO;	
		1			DERWENT;	
		ļ			IBM TDB	
١.	_	1	. 0	(self?programmable same integrated adj2	USPAT;	2003/06/02 14:57
			•	circuit) and ((soft adj macro) and ((hard	US-PGPUB;	
			į	adj macro) or (firm adj macro)))	EPO; JPO;	
İ				adj macro, or (film adj macro,)	DERWENT;	
					IBM_TDB	2003/06/02 14:58
•	-	ļ	0	((design\$3 adj4 integrated adj circuit) and	USPAT;	2003/06/02 14:56
				((optimiz\$3 adj4 integrate adj2 circuit) or	US-PGPUB;	
				(optimiz\$3 adj3 design\$3))) and ((soft adj	EPO; JPO;	
-				macro) and ((hard adj macro) or (firm adj	DERWENT;	
				macro)))	IBM_TDB	
	_		6	(design\$3 adj4 integrated adj circuit) and	USPAT;	2003/06/02 14:58
				((soft adj macro) and ((hard adj macro) or	US-PGPUB:	
				(firm adj macro)))	EPO; JPO;	
-				(IIIIII adj macio///	DERWENT;	
1					·IBM TDB	
				44 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4		2003/06/02 15:04
- -			7	((optimiz\$3 adj4 integrate adj2 circuit) or	USPAT;	2003/06/02 15:04
			,	(optimiz\$3 adj3 design\$3)) and ((soft adj	US-PGPUB;	
ľ				macro) and ((hard adj macro) or (firm adj	EPO; JPO;	
				macro)))	DERWENT;	
					IBM_TDB	, in the second
	-		8	self?programmable same integrated adj2	USPAT;	2003/06/02 15:37
				circuit	US-PGPUB;	
					EPO; JPO;	
					DERWENT;	
					IBM TDB	
			10467	design\$3 adj4 integrated adj circuit	USPAT;	2003/06/02 15:16
	-		12467	designss adj4 integrated adj circuit	US-PGPUB;	2003, 50, 62 15:15
.					EPO; JPO;	
					, ,	
					DERWENT;	
-					IBM_TDB	0000100100 15 00
	-		38	(soft adj macro) and ((hard adj macro) or	USPAT;	2003/06/02 15:33
				(firm adj macro))	US-PGPUB;	
		•			EPO; JPO;	
					DERWENT;	
					IBM TDB	
	_		19	optimiz\$3 and ((soft adj macro) and ((hard	USPAT;	2003/06/02 15:34
			1	adj macro) or (firm adj macro)))	US-PGPUB;	
1				adj macro, or trrim adj macro,,	EPO; JPO;	
	•				DERWENT;	
			1		IBM_TDB	2002/06/02 15:29
- 1.	-		0		USPAT;	2003/06/02 15:38
ľ			1	((optimiz\$3 adj4 integrate adj2 circuit) or	US-PGPUB;	
1				(optimiz\$3 adj3 design\$3))) and	EPO; JPO;	
İ	•		1	self?programmable	DERWENT;	· ·
					IBM_TDB	
	_		2	((optimiz\$3 adj4 integrate adj2 circuit) or	USPAT;	2003/06/02 15:38
			1	(optimiz\$3 adj3 design\$3)) and	US-PGPUB;	
			-	self?programmable	EPO; JPO;	
				SCII. Programmabic	DERWENT;	
1					IBM TDB	,
1]	(2		2003/06/02 15:38
			2 -	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	USPAT;	2003/00/02 15:30
				self?programmable	US-PGPUB;	
			1		EPO; JPO;	
			1		DERWENT;	
- 1				· · · · · · · · · · · · · · · · · · ·	IBM TDB	
- 1						

	68	self?programmable	USPAT;	2003/06/02 15:39
			US-PGPUB;	
	1		EPO; JPO;	
			DERWENT;	
		· ·	IBM TDB	
		a	USPAT;	2003/06/03 07:45
•	2447	design adj3 environment		2003/06/03 07:45
		·	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	•
			IBM_TDB	
-	274323	optimiz\$3	USPAT;	2003/06/03 07:30
			US-PGPUB;	
			EPO; JPO;	
		· ·	DERWENT;	•
			IBM TDB	• •
	660	(design adj3 environment) and optimiz\$3	USPAT;	2003/06/03 07:30
	""	(333-331 331 331 331 331 331 331 331 331 331 331 331 331 331 331	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
	172	and //degion adia environment\ and	USPAT;	2003/06/03 07:30
-	1/2		1	2003/00/03 07:30
		optimiz\$3)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
	111	hard and (macro and ((design adj3	USPAT;	2003/06/03 07:30
		environment) and optimiz\$3))	US-PGPUB;	-
		·	EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	47	soft and (hard and (macro and ((design adj3	USPAT;	2003/06/03 07:31
		environment) and optimiz\$3)))	US-PGPUB;	
			EPO; JPO;	
	•		DERWENT;	
			IBM TDB	
	1000	design add environment	USPAT;	2003/06/03 07:49
-	1080	design adj environment	US-PGPUB;	2003/00/03 07.43
•		·	1	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
•	54		USPAT;	2003/06/03 07:4
	1	(firm adj macro))	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
	. 2	(design adj environment) and ((soft and	USPAT;	2003/06/03 07:47
		macro) and ((hard adj macro) or (firm adj	US-PGPUB;	
	'	macro)))	EPO; JPO;	
		macto///	DERWENT;	
			IBM TDB	
		(1)	_	2002/06/02 07:41
	28	(design adj environment) same optimiz\$3	USPAT;	2003/06/03 07:49
			US-PGPUB;	
			EPO; JPO;	
		· ·	DERWENT;	
	1		IBM TDB	1